## Notice of References Cited Application/Control No. 09/985,768 Examiner Vuthe Siek Applicant(s)/Patent Under Reexamination BOATENG, KWAME OSEI Page 1 of 1

## **U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	Α	US-			
	В	US-	-		
	С	US-	-		
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	-	US-			
	J	US-			·
	К	US-			
	ال	US-			
	М	US-		·	

## **FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	Z					
	0	•			·	
	P					
	α					
	R					
	S					
	Т					

## **NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)				
	U	Pomeranz et al., "On Static Compaction of Test Sequences for Synchronous Sequential Circuits," ACM, 1996, pages 1-6.				
	V	Hamzaoglu et al., "Test Set Compaction Algorithms for Combinational Circuits*," ACM 1998, pages 283-289.				
	w					
	x					

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)

Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.